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number.			Application / Conf.		10/600,848	7556	
TRANSMITTAL			Filing Date		June 19, 2003		
FORM			First Named Inven		Jonathan B. Ballagh		
(to be used for all correspondence after initial filing)			Examiner Name		Andre Pierre Louis		
Mail Stop: AMENDMENT			Art Unit		2123		
Express Mail			Patent No.				
Receipt No. Total Number of Pages in This Submission			Attorney Docket N	umber	X-1192 US		
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Preliminary Amendment After Final		Drawing(s)		٦	<u> </u>		
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X-1192 US -10/600,848

Conf. No.: 7556

PATENT

IN THE UNITED STATES PATENT OFFICE

Jonathan B. Ballagh et al.

Assignee: Xilinx, Inc.

Title: "Clock Stabilization Detection for Hardware Simulation"

Serial No.: 10/600,848 Filed: 06-19-2003

Examiner: Andre Pierre Louis Art Unit: 2123

Docket No.: X-1192 US Conf. No.: 7556

Mail Stop AMENDMENT COMMISSIONER FOR PATENTS P.O. Box 1450

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RESPONSE TO SECOND OFFICE ACTION

Dear Sir:

In response to the Second Office Action mailed from the Patent Office on November 17, 2006, please consider the following remarks.

Listing of the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.